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How How Did I Learn Electronics

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Active Filters

Inverting Amplifier

Frequency Response

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The Art of Electronics

ARRL Handbook

Electronic Circuits

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Introduction

What happens to output pins

Impedance vs frequency

Different packages

Testing

Service Mounts

Outro

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Introduction

BJT Circuits

Schematic

Saturation

Analysis

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Unity Gain Buffer

Differentiation

Nonlinear Functions

Precision Rectifier

Time Domain Behavior of the Circuit

A Precision Rectifier

Fundamental Properties of the Op-Amp

Precision Rectifier

Measuring the Signal Strength

The Current Flowing through R1

Inverting Input

How an Op-Amp Can Be Used in Filter Design

Filter Using Op Amps

Finite Gain

Dc Offsets

Offset Voltage

Okay Now because the Sign Is Also Random It Doesn't Matter whether You Put this Plus Here or Here or whether You Place this Voltage Source in Series with a Non-Inverting Input or in Series with the Inverting Input It Doesn't Make a Difference because Vo S It Has Could Be Positive Could Be Negative Anyway so the Os Can Be Placed in Series with either Input Right It Doesn't Make any Difference So in Fact When We Are Analyzing Circuits Including the Offset Voltage We Pick the Terminal That's More Convenient for Analysis so We Might Place It in Series with this Guy What Is this with this Guy Depending on What the Circuit Is Doing All Right so It's Important To Remember these about the Offset Voltage

And It Seems to Me That Should Be Here So I'LI Place the Offset Voltage Here Vo S and Then of Course I Have V In as My Main Input Alright so We Go Ahead and Build the Circuit and We Would Like To See What the Output Contains Well because We Have Two Voltage Sources in Series We Can Just Add Them Up or if You Don't Like You Can Use Superposition so the Total Voltage That I Measure from Here to Ground Is Vo S + V in So Be Out Is the Total Voltage V in + Vo S Times 1 plus R 1 R 2 over R 1 as We Saw Before

So the Total Voltage That I Measure from Here to Ground Is Vo S + V in So Be Out Is the Total Voltage V in + Vo S Times 1 plus R 1 R 2 over R 1 as We Saw before Okay So this Says that in a Non-Inverting Amplifier if We Have an Offset Voltage in the Rpm That Offset Comes Out Amplified Just the Way the Input Signal Is Amplified if the Input Signal Is Amplified by a Factor of 4 the Offset Is Also Amplified by a Factor of 4

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Introduction of Op Amps

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Introduction to Op Amps

NPN Transistor in Active Mode || Exercise 6.1, 6.2, and 6.3 || EDC 6.1.2(3)(Sedra) - NPN Transistor in Active Mode || Exercise 6.1, 6.2, and 6.3 || EDC 6.1.2(3)(Sedra) by Electrical Engineering Academy 3,090 views 3 years ago 9 minutes, 26 seconds - EDC 6.1.2(3)(**Sedra**,) || Exercise 6.1 || Exercise 6.2 || Exercise 6.3 . NPN Transistor in Active Mode 6.1 Consider an npn transistor ...

Design a Circuit to provide output voltage of 2.4 V || Exercise D 4.11(Sedra 6th Ed) || EDC 4.3.6 - Design a Circuit to provide output voltage of 2.4 V || Exercise D 4.11(Sedra 6th Ed) || EDC 4.3.6 by Electrical Engineering Academy 4,536 views 2 years ago 7 minutes, 12 seconds - Exercise D 4.11 (**Sedra 6th Ed**,) || (English) Design the circuit in Fig. E4.11 to provide an output voltage of 2.4 V.

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